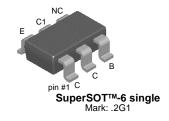


## FMBSA56

## **PNP General Purpose Amplifier**

- This device is designed for general purpose amplifier applications at collector currents to 300 mA.
- · Sourced from Process 73.



# **Absolute Maximum Ratings\*** T<sub>a</sub>=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CEO</sub>	Collector-Emitter Voltage	-80	V
V <sub>CBO</sub>	Collector-Base Voltage	-80	V
V <sub>EBO</sub>	Emitter-Base Voltage	-4.0	V
I <sub>C</sub>	Collector Current - Continuous	-500	mA
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range	- 55 ~ 150	°C

<sup>\*</sup> These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

- These ratings are based on a maximum junction temperature of 150 degrees C.
  These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

### Electrical Characteristics T<sub>a</sub>=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
Off Characte	eristics	•	•		•
V <sub>(BR)CEO</sub>	Collector-Emitter Sustaining Voltage *	$I_C = -1.0 \text{mA}, I_B = 0$	-80		V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	$I_{C} = -100 \mu A, I_{E} = 0$	-80		
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	$I_E = -100\mu A, I_C = 0$	-4.0		V
I <sub>CEO</sub>	Collector Cut-off Current	V <sub>CE</sub> = -60V, I <sub>B</sub> = 0		-0.1	μΑ
I <sub>CBO</sub>	Collector Cut-off Current	$V_{CB} = -80V, I_{E} = 0$		-0.1	μΑ
On Characte	eristics	•	•		•
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = -10mA, V <sub>CE</sub> = -1.0V	100		
		$I_C = -100 \text{mA}, V_{CE} = -1.0 \text{V}$	100		
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	$I_C = -100 \text{mA}, I_B = -10 \text{mA}$		-0.25	V
V <sub>BE(on)</sub>	Base-Emitter On Voltage	I <sub>C</sub> = -100mA, V <sub>CE</sub> = -1.0V		-1.2	V
Small Signa	I Characteristics	•	-		
f <sub>T</sub>	Current Gain Bandwidth Product	$I_C = -10 \text{mA}, V_{CE} = -2.0 \text{V},$ f = 100 MHz	50		MHz
Pulse Test: Pulse	Width ≤ 300μs, Duty Cycle ≤ 2.0%	•	•	•	

### Thermal Characteristics T<sub>a</sub>=25°C unless otherwise noted

Symbol	Parameter	Max.	Units
P <sub>D</sub>	Total Device Dissipation *	700	mW
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, total	180	°C/W

<sup>\*</sup> Device mounted on a 1 in 2 pad of 2 oz copper.

# **Typical Characteristics**

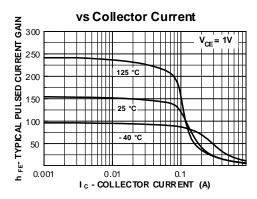


Figure 1. Typical Pulsed Current Gain vs Collector Current

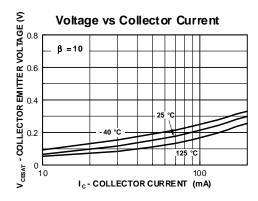


Figure 2. Collector-Emitter Saturation Voltage vs Collector Current

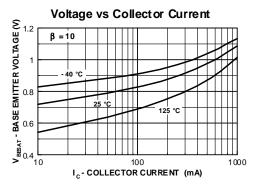


Figure 3. Base-Emitter Saturation Voltage vs Collector Current

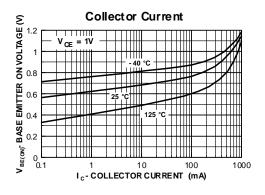


Figure 4. Base-Emitter On Voltage vs Collector Current

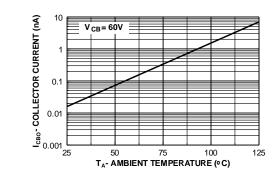


Figure 5. Collector Cutoff Current vs Ambient Temperature

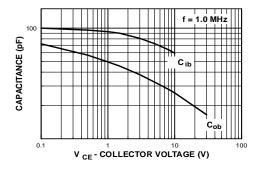


Figure 6. Collector Saturation Region

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# Typical Characteristics (Continued)

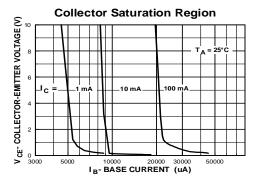


Figure 7. Collector-Emitter Breakdown Voltage with Resistance Between Emitter-Base

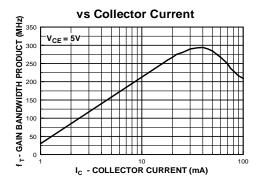
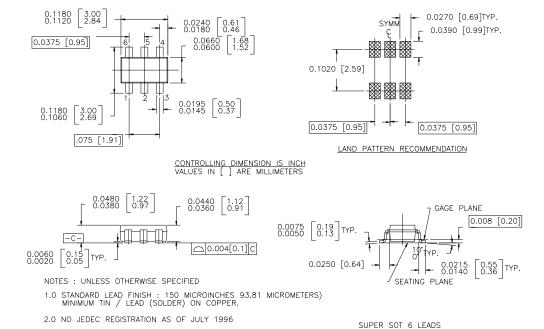


Figure 8. Input and Output Capacitance vs Reverse Voltage

# **Package Dimensions**

# SuperSOT™-6



Dimensions in Millimeters

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DOME™	GTO™	MICROWIRE™	QS™	SyncFET™
EcoSPARK™	HiSeC™	MSX™	QT Optoelectronics™	TinyLogic <sup>®</sup>
E <sup>2</sup> CMOS™	$I^2C^{TM}$	MSXPro™	Quiet Series™	TINYOPTO™
EnSigna™	i-Lo™	OCX™	RapidConfigure™	TruTranslation™
FACT™	ImpliedDisconnect™	OCXPro™	RapidConnect™	UHC™
FACT Quiet Series™		OPTOLOGIC <sup>®</sup>	μSerDes™	UltraFET <sup>®</sup>
Across the board. Around the world.™		OPTOPLANAR™	SILENT SWITCHER®	VCX <sup>TM</sup>
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### PRODUCT STATUS DEFINITIONS

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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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